Detailed project schedules and tollgates. Every week there is a 3 hour lab session (with faculty) + an equivalent time of autonomous work

EE project schedule.

	Week		T2 (wednesday 8-11)	
	1	18 S	Course introduction	Introduction
			Case presentation	Basic knowledge
			Advanced information gathering seminar	Project specification
	2	25 S	Amplifier classes puzzle	
Ă			Project management	
(2)	3	20	Specific amplifier puzzle	
3			Project specifications and tasks assignment	
			Amplifier blocks behavioral modeling	
	4	90	Amplifier behavioral model integration	Design
	5	16 0	Amplifier blocks design	Prototyping
	6		Amplifier blocks characterization	Characterization
	_	23 O	Amplifier integration and characterization	
5	7	30 O	Prototyping	
	8	6 N	Prptotype characterization	
	9	13 N		
	10	20 N	System improvement	Improvement
	11	27 N	Accessory elements design Accessory elements implementation	Finishing Final characterization
	12	4 D	Final characterization	Tillar characterization
	13	11 D	Pre-equalization integration	
			Finishing tasks,	
6	14	18 D	Results presentation, Final Design Review	Results presentation

⁻ Individual project knowledge test. Date to be determined around week 7-8

Project tollgates

, 0							
#		Deliverables					
1	2	Puzzle 1					
2	3	Puzzle 2, 1st Requirement Specification					
3	3-4	Requirement specification, blocks models, timeplan proposal					
4	4-5	Project plan, PDR					
5	9	First prototype evaluation. Progress meeting 1. CDR					
6	14	Progress meeting 2. FDR					

AV project schedule.

	Week		T2 (wednesday 8-11)	
	1	18 S	Course introduction	Introduction
			Case presentation	Basic knowledge
1			Advanced information gathering seminar	Project specification
	2	25 S	Quality and Nonlinearities in audio amp. measurement	
			Project management	
	3	20	Specific Quality and Nonlinearities+EQ+Char. puzzle	
			Project specifications and tasks assignment	
3			Measurement System blocks behavioral modeling	
X	4	9 O	Measurement System+EQ+Simulator: model integration	Design
	5	16 O	Measurement System+EQ+Simulator: blocks design	Prototyping
	6	23 O	Measurement System+EQ+Simulator development	Characterization
	7	30 O	(Indivudual block Tests included)	
(5)	8	6 N	Measurement System+EQ+Simulator	
			Amplifier + loudspeaker characterization	
			Pre-equalizer design + Characterization	
	9	13 N	System improvement	Integration,
6	10	20 N	Accessory elements design	Improvement
	11	27 N	Accessory elements implementation	Finishing
	12	4 D	Final acoustic characterization	Final
V	13	11 D	Pre-equalization implementation	characterization
			Finishing tasks	
\Diamond	14	18 D	Results presentation. Final Design Review	Results presentation

⁻ Individual project knowledge test. Date to be determined around week 6-8